Trip event on August 25th, 2016 at 18:54:02

SSU tripped due to unbalance of the E-C-E circuit Half coil signals. The threshold was set to 200 mV. Figure 1 – 3 shows the bucked Half coil signals; different zooms were used. Figure 4 – 6 shows the E1 current signal; different zooms were used.

It looks like the E1 circuit current started to change about the same time (-~60 msec) when the bucked Half Coil signals show a clear change (see figure 3 and Figure 6). It is quite likely the E1 PS misbehaved and tried to change the current ramp direction. The actual trip however occurred due to a sudden and very fast voltage change. This also could be caused by the PS supply but it is hard to verify it.

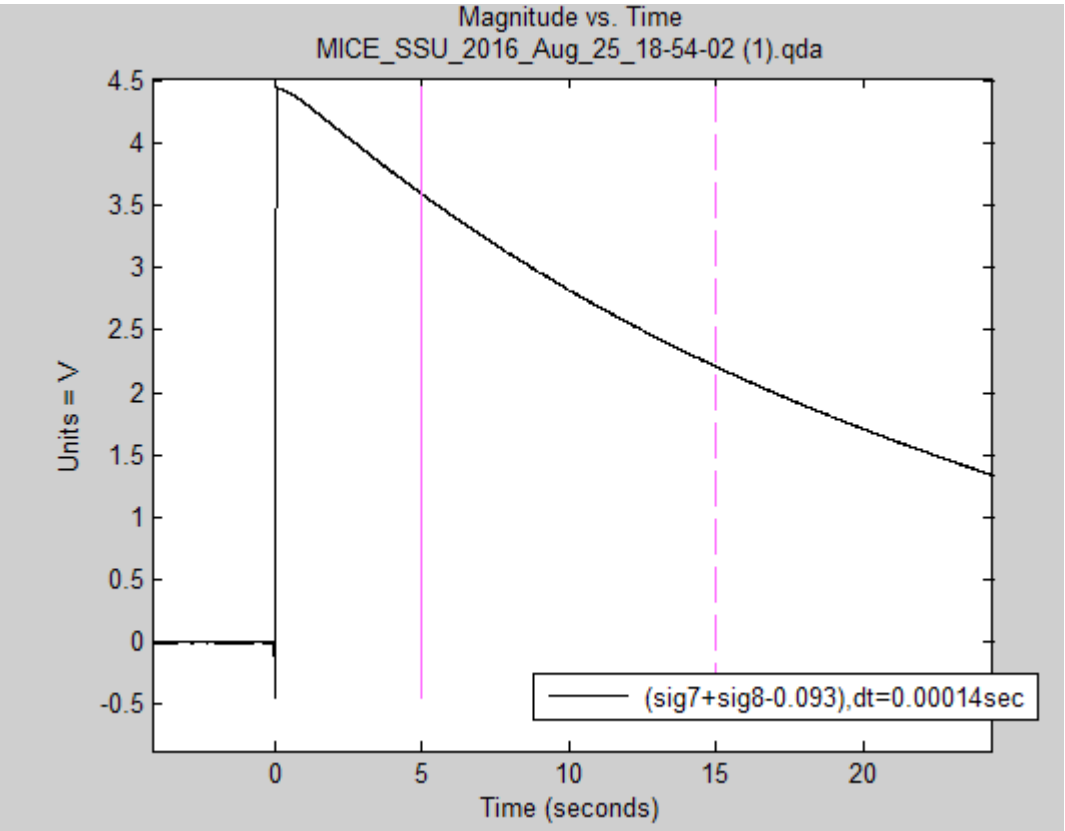

Figure 1. Shows the Bucked half coil signals. At t=0 the circuit tripped.

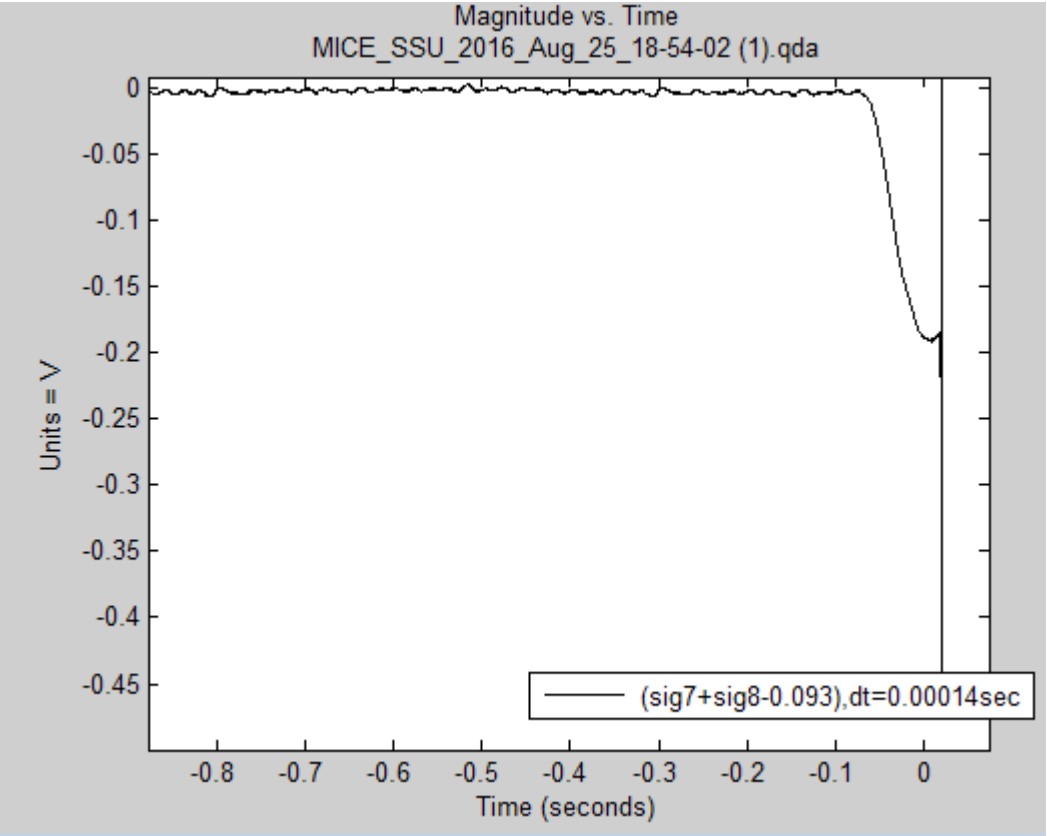

Figure 2. Same signals as Figure 1 zoomed in.

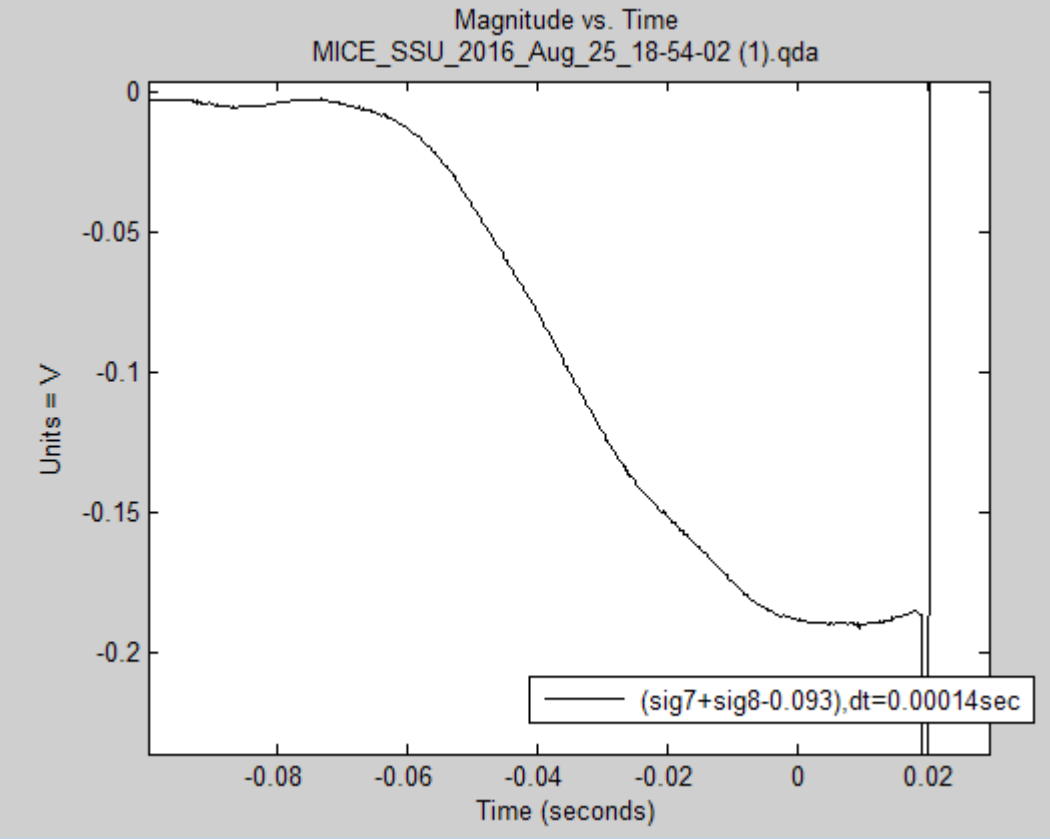

Figure 3. Same signals as Figure 1 and 2 just further zoomed in.

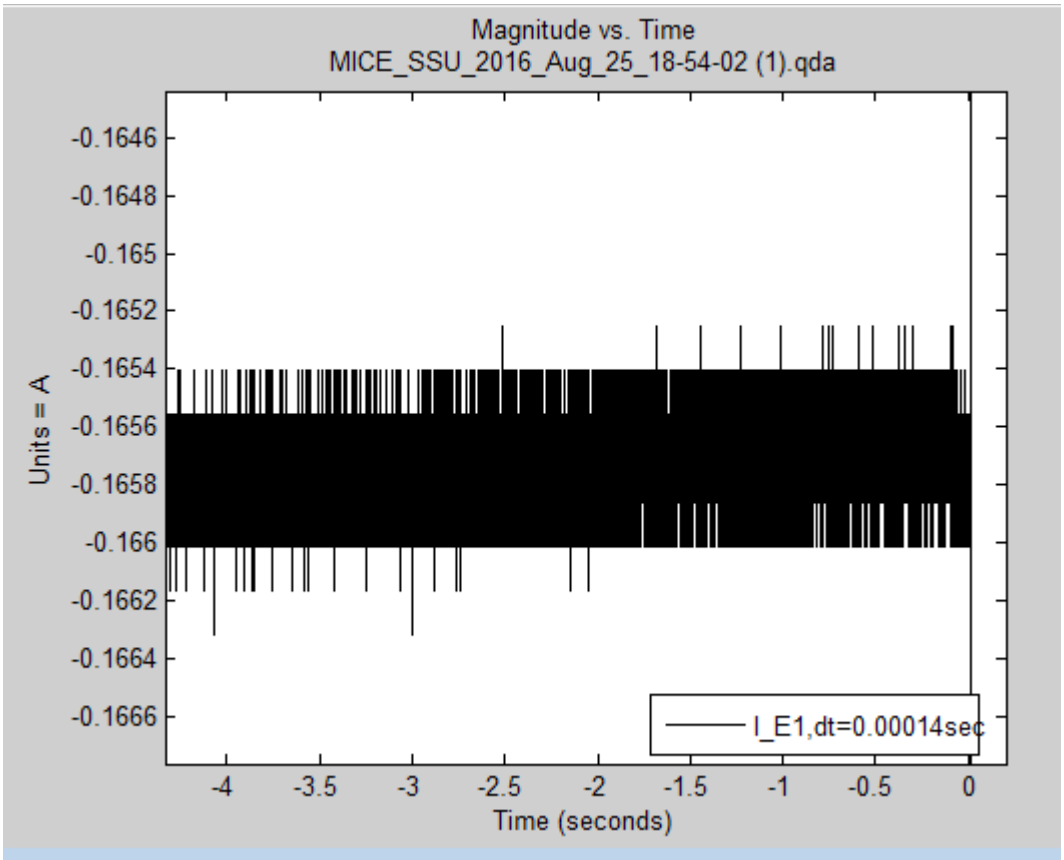

Figure 4. DCCT current signal divided by 100 is shown.

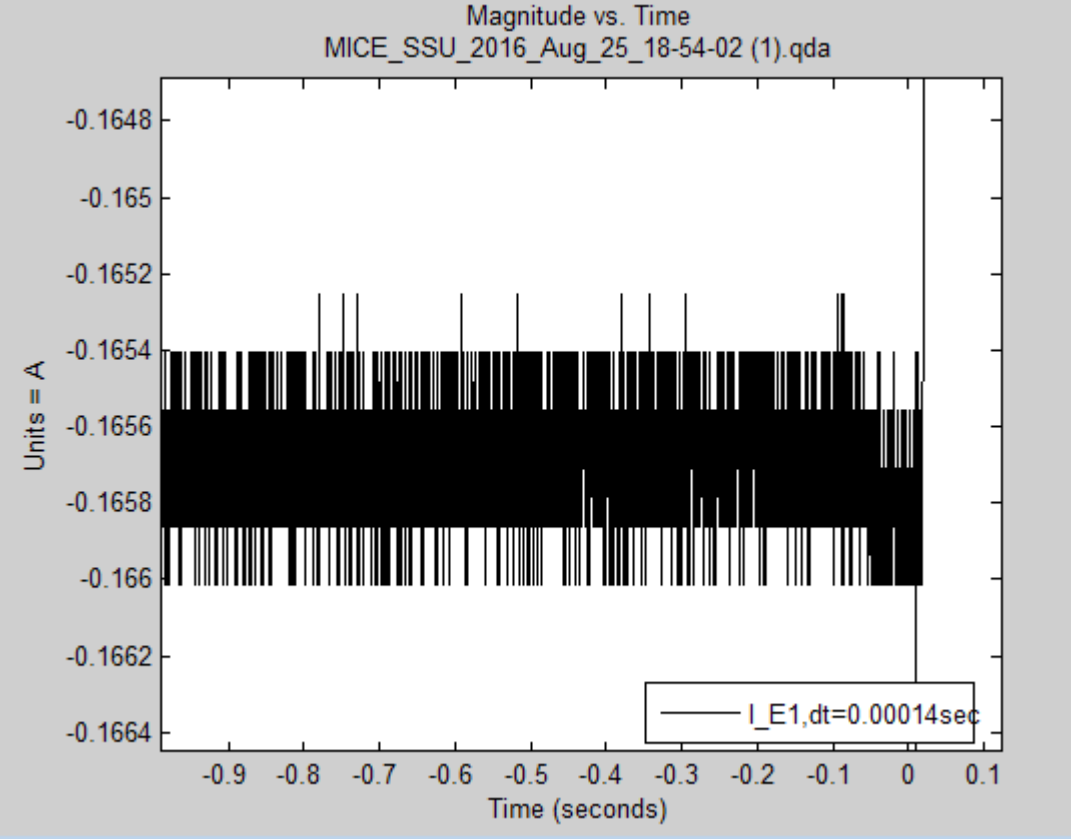

Figure 5. Same signals as Figure 4 zoomed in.

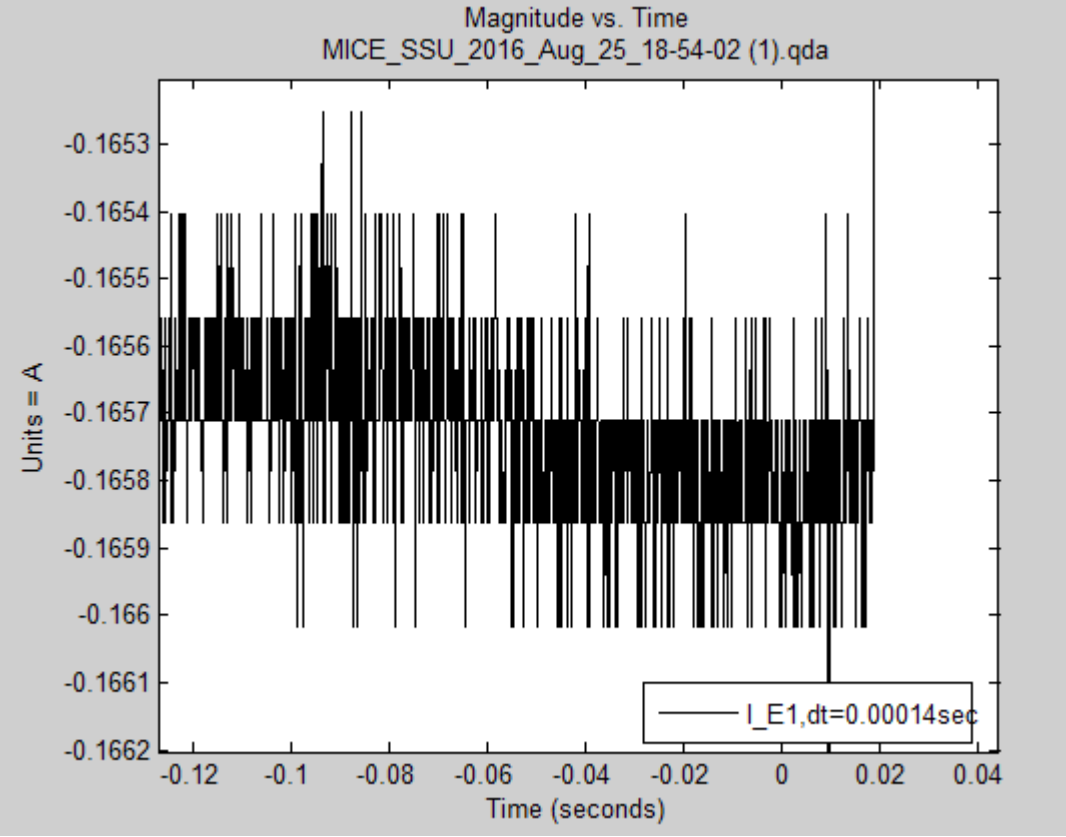

Figure 6. Same signals as Figure 4 and 5 just further zoomed in.